

App. No. 10/658,024
Amdt. Dated February 28, 2005
Reply to Office Action of November 29, 2004
Atty. Dkt. No. 7719-115

Amendment to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (canceled)

Claim 2 (previously presented): A method according to claim 8, wherein said control signal is a reset signal for resetting the given one of the computer units.

Claim 3 (original): A method according to claim 2, wherein said control signal is indicative of either controlling the power to the given one of the computer units or requesting it to trigger a reset mode of operation.

Claim 4 (canceled)

Claim 5 (canceled)

Claim 6 (previously presented): A method according to claim 8, wherein said control signal is a reset signal generated when the condition is determined to be outside of the predetermined limits.

Claim 7 (previously presented): A method according to claim 8, wherein said sensing device is a temperature sensing device.

Claim 8 (currently amended): A method of controlling a group of computer units mounted on a rack, comprising:

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receiving ~~[[a]]~~ computer unit performance signals at a reset control module mounted on the rack;

generating a computer unit control signal in response to the receipt of the performance signal for a given ~~control~~ computer unit;

sending the control signal to the given one of the computer units; ~~for causing it to be controlled in response thereto;~~

receiving the control signal by the given one of the computer units;

causing the operation of the given one of the computer units to be controlled in response to the receipt of the control signal;

wherein said ~~receiving~~ computer unit performance signals are received from a sensing device mounted on the rack for detecting malfunctions;

determining whether the condition is within pre-determined limits; and

sending an alarm message to a remote computer to indicate that a malfunction has occurred when it is determined that the condition is outside said limits.

Claim 9 (original): A method according to claim 8, wherein said message is sent after a predetermined time delay following the determination that the condition is outside said limits.

Claim 10 (original): A method according to claim 9, further including repeating the sending of the alarm message after another time delay interval.

Claim 11 (new): A method of controlling a group of computer units mounted on a rack, comprising:

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receiving computer unit performance signals;
generating a computer unit control signal in response to the receipt of the performance signal for a given computer unit;
sending the control signal to the given one of the computer units;
receiving the control signal by the given one of the computer units; and
causing the operation of the given one of the computer units to be controlled in response to the receipt of the control signal.

Claim 12 (new): A system of controlling a group of computer units mounted on a rack, comprising:

means for receiving computer unit performance signals;
means for generating a computer unit control signal in response to the receipt of the performance signal for a given computer unit;
means for sending the control signal to the given one of the computer units;
means for receiving the control signal by the given one of the computer units;
and
means for causing the operation of the given one of the computer units to be controlled in response to the receipt of the control signal.

Claim 13 (new): A system of controlling a group of computer units mounted on a rack, comprising:

a first receiver for receiving computer unit performance signals;

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a control circuit for generating a computer unit control signal for a given computer unit in response to the receipt of the performance signal by the receiver,
a transmission circuit for sending the control signal from the generator to the given one of the computer units;
a second receiver operatively coupled to the given one of the computer units for receiving the control signal; and
a computer unit circuit for causing the operation of the given one of the computer units to be controlled in response to the receipt of the control signal via the second receiver.